Test Booklet Code & Serial No.

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प्रश्नपत्रिका कोड व क्रमांक Paper-II ELECTRONIC SCIENCE

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Sigi	nature and Name of Invigilator	Seat No.							
1. (S	lignature)					٠.		•••	
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		Seat No				•••••	• • • • • • •		
2. (S	lignature)		(In	word	ls)				
(N	Name)O	MR Sheet No.							
AP	PR - 38224		<u>(Та</u>	be f	illed	bv tl	he Ca	andid	late)
Tim	ne Allowed : 2 Hours]		(1)			•		rks :	
	nber of Pages in this Booklet : 24	Number of	Que						
1.	Instructions for the Candidates Write your Seat No. and OMR Sheet No. in the space provided	1. परिक्षार्थींनी उ				या सूच- प्डावरील		कोप-यात	लिहावा.
2.	on the top of this page. This paper consists of 100 objective type questions. Each question	तसेच आपण	ंस दिलेल	या उत्तरप	त्रिकेचा	क्रमांक त	त्याखाली	लिहावा.	
	will carry two marks. All questions of Paper II will be compulsory.	2. सदर प्रश्नपरि							दोन गुण
3.	At the commencement of examination, the question booklet will be given to the student. In the first 5 minutes, you are	आहेत. या प्र 3. परीक्षा सुरू इ							ातीच्या ५
	requested to open the booklet and compulsorily examine it as follows:	मिनीटांमध्ये	आपण सद	दर प्रश्नप	त्रिका उ	घडून खा	लील बा	बी अवश्य	य तपासून
	(i) To have access to the Question Booklet, tear off the	पहाव्यातः							355
	paper seal on the edge of this cover page. Do not accept a booklet without sticker-seal or open booklet.		नपत्रिका व						
	(ii) Tally the number of pages and number of questions in		न नसलेलं ल्या पृष्ट						
	the booklet with the information printed on the cover page. Faulty booklets due to missing pages/questions		च प्रश्नप						
	or questions repeated or not in serial order or any	पृष्टे	उकमी अ	सलेली/	कमी प्रश	न असले	ली/प्रश्न	गंचा चुक	ीचा क्रम
	other discrepancy should not be accepted and correct booklet should be obtained from the invigilator within		लिली कि						
	the period of 5 minutes. Afterwards, neither the Question		न् निटातच्						
	Booklet will be replaced nor any extra time will be given. The same may please be noted.		वी. त्यानं व्वृन मिळ						
	(iii) After this verification is over, the OMR Sheet Number		जून निळ लिप्रमाणे						
4.	should be entered on this Test Booklet. Each question has four alternative responses marked (A), (B),		एम.आर.						
1.	(C) and (D). You have to darken the circle as indicated below on	4. प्रत्येक प्रश्ना	साठी (A),	, (B), (C) आणि	(D) अश	ी चार वि		
	the correct response against each item. Example : where (C) is the correct response.	आहेत. ृत्यार्त		उत्तराच	रकाना	खाली व	दर्शविल्य	गप्रमाणे व	उळक पणे
		काळा/निळा व		 .	 .	-			
_	(A) (B) (D)	उदा. : जर ((ડ) દ યાવ્ય		\ 4		\frown		
5.	Your responses to the items are to be indicated in the OMR Sheet given inside the Booklet only. If you mark at any place		(A)	В	/		D	_	
	other than in the circle in the OMR Sheet, it will not be evaluated.	 या प्रश्नपत्रिवं 	व्यक्तिल प्रश्न	नाची उत्त	रे ओ.एम	म.आर. ट	त्तरपत्रिव -	केतच दश	गेवावीत,
6. 7.	Read instructions given inside carefully. Rough Work is to be done at the end of this booklet.	इतर ठिकाणी वि 6. आत दिलेल्या							
8.	If you write your Name, Seat Number, Phone Number or put	6. आत दिलेल्या 7. प्रश्नपत्रिकेच	। सूचना प गा शेतरी	ठाळजापू जोटलेल	श्रक श्राष्ट्र या कोड्य	गण्यातः गणनात्रा	च कच्चे	குற க	ਸਕੇ
	any mark on any part of the OMR Sheet, except for the space	8. जर आपण अ	ग राजटा ग्रे.एम.आ	गाउँहार र. वर न	गा गा ग मद केले	ल्या ठिक	.च चम्ब जणा व्यवि	वगन वग तरीक्त इत	राजः ए कोतेही
	allotted for the relevant entries, which may disclose your	नाव, आसन							
	identity, or use abusive language or employ any other unfair means, you will render yourself liable to disqualification.	केलेली आढ	ळून आल्य	ास अथव	न्ना असभ्य	य भाषेचा	वापर कि	वा इतर रै	
9.	You have to return original OMR Sheet to the invigilator at the	अवलंब केल							1960 1960
	end of the examination compulsorily and must not carry it with	9. परीक्षा संपल्य							
	you outside the Examination Hall. You are, however, allowed to carry the Test Booklet and duplicate copy of OMR Sheet on	परत करणे अ							पत्रिकची
	conclusion of examination.	द्वितीय प्रत अ 10. फक्त निळ्या						આह.	
10.	Use only Blue/Black Ball point pen.	10. फक्त निळ्या 11. कॅलक्युलेटर) .	
11.	Use of any calculator or log table, etc., is prohibited.	11. चुकीच्या उत्त						M	
12.	There is no negative marking for incorrect answers.	9		na 1000 Aniila	100000000000000000000000000000000000000	med could distin	- Control		

12.

There is no negative marking for incorrect answers.

Electronic Science Paper II

Time Allowed: 120 Minutes] [Maximum Marks: 200 Note: This Paper contains Hundred (100) multiple choice questions. Each question carrying Two (2) marks. Attempt All questions.

- 1. How is the complement nature of NOR represented in its graphic symbol ?
 - (A) A small square
 - (B) A small triangle
 - (C) A small circle
 - (D) A small diamond
- 2. What does SPLD stand for in the context described ?
 - (A) Simple Programming Logic

 Device
 - (B) Sequential Programmable Logic

 Device
 - (C) Systematic PLD
 - (D) Structured Programming and Logic Device

- 3. Which statement accurately describes the programming of an FPGA?
 - (A) FPGA programming is permanent and cannot be changed.
 - (B) FPGA programming is done using machine code
 - (C) FPGA programming is done using hardware description languages (HDLs) or schematic entry
 - (D) FPGA programming is only possible in a controlled laboratory environment
- 4. What is the role of the Global Clock Network in CPLD?
 - (A) It synchronizes all clock signal in the devices
 - (B) It is responsible for configuring the entire devices
 - (C) It has no impact on timing constraints
 - (D) It is used for power consumption

5.	For design of 16:1 MUX how many	8.	Output values of Moore type FSM	
	select lines are required?		are determined by its	
	(A) 1		(A) Clock input	
	(B) 2		(B) Current state	
	(C) 3		(C) Input values	
	(D) 4		(D) Output values	
6.	A decoder converts 'n' inputs to outputs.	9.	VHDL represents a	
			component's external behavior and	
	(A) <i>n</i>		interface.	
	(B) n^2 (C) 2^n		(A) Signal	
			(B) Port	
			(C) Entity	
	(D) n^n		(D) Architecture	
7.	The expression for number of modulo 10	10.	. Race Around condition can be	
	of a ripple counter with 'n' flip-flop		avoided in digital logic circuits	
	is:		using:	
	(A) 2^n		(A) Shift register	
	(B) n		(B) Full adder	
	(C) $2^n - 1$		(C) AND gate	
	(D) n^2		(D) Master slave JK flip-flop	

- 11. The configuration in which each LED receives operating current of 8 mA from power supply while the port lines sink the current on each port line is:
 - (A) driver configuration
 - (B) common anode configuration
 - (C) common cathode configuration
 - (D) buffer configuration
- 12. If no key is pressed microcontroller 8051 port will read :
 - (A) 0
 - (B) 1
 - (C) NC
 - (D) 7
- 13. What is the status of the carry, auxiliary carry and parity flags after executing following instructions?

MOV A,#9C

ADD A.#64H

(A)
$$CY = 0$$
, $AC = 0$, $P = 0$

(B)
$$CY = 1$$
, $AC = 1$, $P = 0$

(C)
$$CY = 0$$
, $AC = 1$, $P = 0$

(D)
$$CY = 1$$
, $AC = 1$, $P = 1$

- 14. What is the frequency of the clock that is being used as the clock source for the timer?
 - (A) some externally applied frequency f'
 - (B) controller's crystal frequency f
 - (C) controller's crystal frequency/12
 - (D) externally applied frequency/12
- 15. In 8086 microprocessor, the address bus is bit wide.
 - (A) 12 bit
 - (B) 10 bit
 - (C) 16 bit
 - (D) 20 bit
- 16. The register AX is formed by grouping
 - (A) BH & BL
 - (B) CH & CL
 - (C) AH & AL
 - (D) DH & DL

- 17. A 20-bit address bus allows access to a memory of capacity :
 - (A) 1 MB
 - (B) 2 MB
 - (C) 4 MB
 - (D) 8 MB
- 18. What is the Address (SFR) for TCON, SCON, SBUF, PCON and PSW respectively?
 - (A) 88H, 98H, 99H, 87H, 0D0H
 - (B) 98H, 99H, 87H, 88H, 0D0H
 - (C) 0D0H, 87H, 88H, 99H, 98H
 - (D) 87H, 88H, 0D0H, 98H, 99H
- 19. In 8051 which interrupt has highest priority?
 - (A) IE1
 - (B) TF0
 - (C) IE0
 - (D) TF1

- 20. Which of the following should a microcontroller at least consist of:
 - (A) CPU, ROM, I/O ports and Timers
 - (B) RAM, ROM, I/O ports and Timers
 - (C) CPU, RAM, I/O ports and Timers
 - (D) CPU, RAM, ROM, I/O ports and Timers
- - (A) Zero
 - (B) Only due to the charge inside that point
 - (C) Only due to the charge outside that point
 - (D) Due to the entire charge of the sphere

- 22. What happens to the current in a coil while accelerating a magnet inside it?
 - (A) Increases
 - (B) Decreases
 - (C) Remains constant
 - (D) Reverses
- 23. In an electromagnetic wave, the electric field of amplitude 6.2 V/m oscillates with a frequency of 2.4×10^{10} Hz. Estimate energy density of the wave :

$$(\epsilon = 8.85 \times 10^{-12}~C^2~/~Nm^2)$$

- (A) $1.4 \times 10^{-10} \text{ J/m}^3$
- (B) $2.4 \times 10^{-10} \text{ J/m}^3$
- (C) $3.4 \times 10^{-10} \text{ J/m}^3$
- (D) $4.4 \times 10^{-10} \text{ J/m}^3$
- 24. The ratio of conduction to displacement current density is referred to as:
 - (A) Attenuation constant
 - (B) Propagation constant
 - (C) Loss tangent
 - (D) Dielectric constant

- 25. Which component of the electric field intensity is always continuous at the boundary?
 - (A) Tangential
 - (B) Normal
 - (C) Horizontal
 - (D) Vertical
- 26. Calculate the skin depth of a material with attenuation constant of 2 units.
 - (A) 2
 - (B) 1
 - (C) 0.5
 - (D) 4
- 27. The expression for velocity of a wave in the conductor is:
 - $(A) \quad V = \sqrt{\left(\, 2\omega \, / \, \mu\sigma \, \right)}$
 - (B) $V = \sqrt{(2\omega\mu\sigma)}$
 - (C) $V = (2\omega / \mu\sigma)$
 - (D) $V = (2\omega\mu\sigma)$

- 28. The standing wave ratio of short circuited and open circuited lines will be:
 - (A) 0
 - (B) 1
 - (C) -1
 - (D) ∞
- 29. In a good conductor the phase relation between the tangential components of electric E and the magnetic field H is as follows:
 - (A) E and H are in phase
 - (B) E and H are out of phase
 - (C) H leads E by 90
 - (D) E leads H by 45
- 30. Consider a transmission line of characteristic impedance 50 ohm.
 Let it be terminated at one end by + j50 ohm. The VSWR produced by it in the transmission line will be:
 - (A) 1
 - (B) 0
 - (C) Infinity
 - (D) + j

- 31. The phase shift keying is a modulation technique used for transmitting:
 - (A) digital data over analog channel
 - (B) analog data over digital channel
 - (C) binary signals only
 - (D) FM signals
- 32. A PLL maintains lock by comparing:
 - (A) The phase of two signals
 - (B) The frequency of two signals
 - (C) The amplitude of two signals
 - (D) The amplitude of input signal with a fixed threshold
- 33. To increase level of very weak radio signals from an antenna, you would use:
 - (A) an RF oscillator
 - (B) an audio oscillator
 - (C) an RF amplifier
 - (D) an audio amplifier

- 34. Which is the true statement about frequency deviation in frequency modulation?
 - (A) frequency deviation is proportional to carrier signal frequency
 - (B) frequency deviation is proportional to amplitude of carrier signal
 - (C) frequency deviation is proportional to modulating frequency
 - (D) frequency deviation is proportional to amplitude of modulating signal
- 35. The equivalent noise temperature of a network given the noise figure of the network or system is:
 - (A) $T_0 (F 1)$
 - (B) $T_0 (F + 1)$
 - (C) T_0 (F)
 - (D) T_0/F

- 36. Which is called as on-off keying?
 - (A) Frequency shift keying
 - (B) Uni-polar PAM
 - (C) Amplitude shift keying and Uni-polar PAM
 - (D) Phase shift keying
- 37. Which of the following technology distributes the coverage of the cell and extends the cell boundary to hard-to-reach places?
 - (A) Sectoring
 - (B) Cell splitting
 - (C) Micro cell zone concept
 - (D) Scattering
- 38. A planar LED is fabricated from GaAs is having a optical power emitted, which is 0.018% of optical power generated internally which is 0.6 P. Determine external power efficiency:
 - (A) 0.18%
 - (B) 0.32%
 - (C) 0.65%
 - (D) 0.9%

39.	A technique used for determining
	the total fiber attenuation per unit
	length is method.
	(A) Frank
	(B) Cut-off

40. Global Sensor Network is built for:

(C) Cut-back

(D) OSA

- (A) Reducing cost and time for development
- (B) Reducing cost and increasing time for development
- (C) Increasing cost and increasing time for development
- (D) Increasing cost and decreasing time for development

- 41. The turn-off time of thyristor is 30 m sec at 50°C. Its turn-off time at 100° is:
 - (A) same
 - (B) 15 m sec
 - (C) 60 m sec
 - (D) 100 m sec
- 42. A six pulse thyristor rectifier bridge is connected to a balanced 50 Hz three-phase ac source. Assuming that the dc output current of the rectifier is constant, the lowest harmonic component in the ac source line current is:
 - (A) 100 Hz
 - (B) 150 Hz
 - (C) 250 Hz
 - (D) 300 Hz

- 43. In a thyristor dc chopper, which type of commutation results in best performance ?
 - (A) current commutation
 - (B) load commutation
 - (C) voltage commutation
 - (D) supply commutation
- 44. Consider the following statements:

 The diodes in a voltage source inverter (McMurray Inverter) should be able to:
 - (1) Withstand a large voltage in the reverse direction
 - (2) Carry the commutating current excess of load current
 - (3) Provide the required reversebias to the outgoing thyristor
 - (4) Feedback the reactive current to the source.

 Of these statements:
 - (A) (1), (2) and (3) are correct
 - (B) (1), (3) and (4) are correct
 - (C) (2), (3) and (4) are correct
 - (D) (1), (2) and (4) are correct

- 45. In a dc motor, if the field coils get opened, the speed of the motor will:
 - (A) decrease
 - (B) come to a stop
 - (C) increase
 - (D) become zero
- 46. Voltage induced in the rotor of the induction motor when it runs at synchronous speed is:
 - (A) very near input voltage to stator
 - (B) slip time the input voltage
 - (C) zero
 - (D) very near input voltage to rotor
- 47. Which of the following is an open loop control system?
 - (A) Field controlled D.C. motor
 - (B) Wardleonard control
 - (C) Metadyne
 - (D) Stroboscope

- 48. Any externally introduced signal affecting the controlled output is called a:
 - (A) feedback
 - (B) stimulus
 - (C) signal
 - (D) gain control
- 49. For a type one system, the steadystate error due to step input is equal to:
 - (A) Infinite
 - (B) Zero
 - (C) 0.25
 - (D) 0.5
- 50. In a control system the output of the controller is given to:
 - (A) final control element
 - (B) amplifier
 - (C) comparator
 - (D) sensor

51. Match List I with List II:

List I

- (a) Sensors
- (b) Networks
- (c) Augmented intelligence
- (d) Standards

List II

- (i) Analytical tools that improve the ability to describe phenomenon
- (ii) Commonly accepted prescriptions for action
- (iii) A device that generates an electronic signal from a physical condition
- (iv) A mechanism for communicating an electronic signal

Choose the correct answer from the options given below:

- (a) (b) (c) (d)
- (A) (ii) (i) (iii) (iv)
- (B) (iii) (iv) (ii) (i)
- (C) (iii) (iv) (i) (ii)
- (D) (ii) (i) (iv) (iii)

- 52. Semiconductor strain gauges depend upon for their action.
 - (A) Piezoelectric effect
 - (B) Piezo-resistive effect
 - (C) Ferro-magnetic
 - (D) Superconductivity
- 53. On applying mechanical stresses, a material gets electrically polarized.It must be a :
 - (A) Superconducting material
 - (B) Piezoelectric material
 - (C) Ferro-magnetic material
 - (D) Ferroelectric material
- 54. of a measuring system refers to its ability to follow instant by instant the measured with time.
 - (A) Bandwidth
 - (B) Fidelity
 - (C) Measurement lag
 - (D) Settling time

- 55. What will be the ratio of amplitudes of largest (maximum) signal to smallest (minimum) signal to which the system is subjected?
 - (A) Time constant
 - (B) Settling period
 - (C) Dynamic range
 - (D) Bandwidth
- 56. The sweep generator of a CRO is used to produce :
 - (A) sinusoidal voltage for the horizontal deflection of electron beam
 - (B) sawtooth voltage for the vertical deflection of electron beam
 - (C) sinusoidal voltage for the vertical deflection of electron beam
 - (D) sawtooth voltage for the horizontal deflection of electron beam

57.	Digital voltmeters can be used to	59.	Needle electrode is used to	
	measure			
	(A) voltage only		measure	
	(B) voltage, temperature. pressure		(A) EKG	
	etc.		(B) EEG	
	(C) voltage and current			
	(D) voltage and resistance		(C) EOG	
58.	What is the relation between scale		(D) EMG	
	factor and sensitivity of a transducer?	60.	Which rhythm is the principal	
	(A) Scale factor is double of		component of the EEG that indicates	
	sensitivity		the alertness of the brain?	
	(B) Scale factor is inverse of sensitivity		(A) Theta rhythm	
	(C) Sensitivity is inverse of scale		(B) Gamma rhythm	
	factor		(C) Beta rhythm	
	(D) Sensitivity is equal to scale			
	factor		(D) Alpha rhythm	

- 61. What is the main purpose of a Schottky diode in semiconductor applications?
 - (A) Voltage regulation
 - (B) High-speed switching
 - (C) Temperature sensing
 - (D) Power amplification
- 62. What is the primary advantage of using a Gallium Nitride (GaN) semiconductor over traditional silicon in power electronics?
 - (A) Higher electron mobility
 - (B) Lower bandgap
 - (C) Greater thermal stability
 - (D) Improved resistance to radiation

- 63. What type of backlighting is commonly used in LCD displays for improved color reproduction ?
 - (A) Fluorescent backlighting
 - (B) Incandescent backlighting
 - (C) LASER backlighting
 - (D) OLED backlighting
- 64. What is the purpose of connecting a resistor in series with a Zener diode in a voltage regulator circuit?
 - (A) To increase the breakdown voltage
 - (B) To limit the forward bias current
 - (C) To decrease the reverse bias voltage
 - (D) To provide mechanical stability

- 65. Which type of low-dimensional semiconductor device is known for its ability to confine charge carriers in all three dimensions?
 - (A) Quantum wells
 - (B) Quantum wires
 - (C) Quantum dots
 - (D) Quantum cascade devices
- 66. What is the role of the series resistance in the I-V characteristics of a solar cell ?
 - (A) To reduce the open-circuit voltage
 - (B) To decrease the short-circuit current
 - (C) To limit the current flow in the external circuit
 - (D) To maximize the fill factor

- 67. What is the role of the "Dirac cones" in the electronic structure of graphene?
 - (A) They represent the valence and conduction bands
 - (B) They indicate the presence of a bandgap
 - (C) They show the dispersion relation of electrons
 - (D) They enhance the magnetic properties of graphene
- 68. How does the chirality of a carbon nanotube affect its electronic properties?
 - (A) It determines whether the nanotube is metallic or semiconducting
 - (B) It affects the mechanical strength of the nanotube
 - (C) It influences the thermal conductivity of the nanotube
 - (D) It is unrelated to the electronic properties of nanotubes

- 69. How does ZnO exhibit both semiconducting and piezoelectric properties simultaneously?
 - (A) Through the manipulation of doping concentration
 - (B) Due to its wurtzite crystal structure
 - (C) By controlling the synthesis temperature
 - (D) It does not possess piezoelectric properties
- 70. In an LED, what is the function of the p-n junction?
 - (A) To modulate the intensity of light
 - (B) To amplify the input signal
 - (C) To emit light when forwardbiased
 - (D) To store electrical charge

- 71. Which of the following statements is true in case of CMOS fabrication?
 - (A) Fabrication of n-mos transistor requires few additional steps compared to p-MOS transistor
 - (B) Fabrication on n-MOS is same as that of p-MOS transistor
 - (C) Fabrication on n-MOS is different from that of p-MOS transistor
 - (D) Fabrication of p-MOS transistor requires few additional steps compared to n-MOS transistor
- 72. EDAX is a technique used in conjunction with SEM for :
 - (A) Energy dispersive analysis of X-rays
 - (B) Energy levels occupied by electrons excited by X-rays
 - (C) Electron diffraction analysis of X-ray excitations
 - (D) Obtaining higher magnification levels

73.	The isolated active areas are created	76.	What does CCD stand for ?
	by technique known as		(A) Control Circuit Design
	(A) Selective deposition of field- oxide isolation		(B) Central Core Database
	(B) Local Oxidation of Silicon		(C) Charge-Coupled Device
	(C) Etched field-oxide isolation or		(D) Composite Camera Device
	Local Oxidation of Silicon	77.	As die size shrinks. the complexity
	(D) Reverse biased pn junction		of making the photomasks
74.	The gate delay is proportional to:		(A) increases
	(A) Ron.Cg		(B) decreases
	(B) Rs.Cds		(0)
(C) Rd.Cgs (D) Ron.Cox	(C) Rd.Cgs		(C) remains the same
	(D) Ron.Cox		(D) cannot be determined
75 .	At threshold voltage, the surface	78.	The minimum width of n -diffusion
, , ,	potential is:		and <i>p</i> -diffusion layer should be:
	(A) Negative Fermi potential		(A) 3λ
	(B) Fermi potential		(B) 2λ
	(C) 2 Fermi potential		(C) λ
	(D) –2 Fermi potential		(D) 4λ

7 0	7777		
79.	•	82.	In a parallel resonant circuit, the
	in the reaction chamber for the		impedance is minimum at:
	epitaxial growth process?		(A) Resonant frequency
	(A) Cup		(A) Resoliant frequency
	(B) Boats		(B) Half the resonant frequency
	(C) Ingots		(C) Double the resonant frequency
	(D) Crucible		(D) Quarter the resonant frequency
80.	As per lambda design rules for wires	83.	Norton's equivalent current source
	minimum width of metal 2 is		is connected in :
	and separation between two metal		
	2 layers		(A) Series with the load
	(A) 4 lambda, 4 lambda		(B) Parallel with the load
	(B) 3 lambda, 3 lambda		(C) Series with the source
	(C) 4 lambda, 3 lambda		(D) Parallel with the source
	(D) 3 lambda, 4 lambda	84.	The unit step response of a first-
81.	In an AC circuit, the power factor		order system exhibits:
	is the cosine of the:		(4) 0 17 11
	(A) Voltage angle		(A) Oscillations
	(B) Current angle		(B) Exponential rise
	(C) Phase angle		(C) Exponential decay

(D) Resistance angle

19 [P.T.O.

(D) Sine wave behaviour

- 85. In network synthesis, what is the role of the Hurwitz criterion?
 - (A) To determine the stability of a control system
 - (B) To check the realizability of a network
 - (C) To analyze the frequency response of a circuit
 - (D) To evaluate the accuracy of numerical methods in solving circuits
- 86. The Smith chart is primarily used for the analysis of :
 - (A) Transmission lines
 - (B) Filters
 - (C) Log Amplifiers
 - (D) Oscillators
- 87. What is the purpose of the Bode plot in network analysis?
 - (A) To represent the step response of a system
 - (B) To visualize the frequency response of a system
 - (C) To analyze the transient behavior of a circuit
 - (D) To plot the time domain behavior of a network

- 88. What is the purpose of the Smith predictor in control system design?
 - (A) To compensate for time delays in the system
 - (B) To enhance the transient response of the system
 - (C) To reduce overshoot in the step response
 - (D) To improve the frequency response of the system
- 89. The concept of "duality" in network synthesis refers to:
 - (A) The relationship between input and output signals in a system
 - (B) The interchangeability of resistors and capacitors in a network
 - (C) The dual representation of a network in terms of nodes and branches
 - (D) The ability of a network to exhibit both series and parallel elements

- 90. Consider the signal $x[n] = 2^n u[n]$ having Z-transform as X(z) with ROC R. The inverse Z transform for X(2z) will be:
 - (A) u[n]
 - (B) $2^n u[n]$
 - (C) $4^{u} u[n]$
 - (D) $2^{2n} u[n]$
- 91. What does the h_{FE} value represent in the common-emitter configuration?
 - (A) Reverse current transfer ratio
 - (B) Output impedance
 - (C) Voltage gain
 - (D) Forward current transfer ratio
- 92. How does the Schmitt trigger eliminate the erratic operation in the op-amp comparator ?
 - (A) By reducing the sensitivity
 - (B) By introducing negative feedback
 - (C) By using positive feedback
 - (D) By increasing the reference voltage

- 93. What is the main disadvantage of half-wave rectifier compared to full wave rectifier?
 - (A) Higher cost
 - (B) Lower efficiency
 - (C) Limited voltage output
 - (D) More complex circuitry
- 94. In any circuit connections, what is the biasing direction for the emitter and collector of a transistor?
 - (A) Both are forward biased
 - (B) Both are reverse biased
 - (C) Emitter is forward, and collector is reverse biased
 - (D) Emitter is reverse, and collector is forward biased
- 95. In a CE configuration, an emitter resistor is used for
 - (A) Stabilization
 - (B) AC signal bypass
 - (C) Collector bias
 - (D) Higher gain

- 96. Explain the concept of phase noise in the context of PLLs:
 - (A) Phase noise is the phase deviation between the reference and feedback signals
 - (B) Phase noise is the undesirable random fluctuations in the phase of the output signal
 - (C) Phase noise is the intentional modulation of the VCO frequency
 - (D) Phase noise is the difference in phase between the input and output of the phase comparator
- 97. Oscillators operate on the principles of
 - (A) Signal feedthrough
 - (B) Positive feedback
 - (C) Negative feedback
 - (D) Ultra high gain

- 98. Following circuits can be used as voltage to frequency converter:
 - (A) Astable multivibrator
 - (B) Crystal oscillator
 - (C) Schmitt trigger
 - (D) CE amplifier
- 99. What is the advantage of using IC LM725?
 - (A) Very high gain
 - (B) Very high BW
 - (C) High CMRR
 - (D) High slew rate
- 100. Which of the following units is used to convert square wave into triangular wave?
 - (A) Differentiator
 - (B) Integrator
 - (C) Schmitt trigger
 - (D) Monoshot multivibrator

ROUGH WORK

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